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Relevance scale ☐ ☐ ☐ ☐ ☐**1 Relocating machine instructions by currying**

Norman Ramsey

May 1996 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1996 conference on Programming language design and implementation**, Volume 31 Issue 5

Full text available: pdf(1.19 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Relocation adjusts machine instructions to account for changes in the locations of the instructions themselves or of external symbols to which they refer. Standard linkers implement a finite set of relocation transformations, suitable for a single architecture. These transformations are enumerated, named, and engraved in a machine-dependent object-file format, and linkers must recognize them by name. These names and their associated transformations are an unnecessary source of machine-dependence ...

2 Specifying representations of machine instructions

Norman Ramsey, Mary F. Fernández

May 1997 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 19 Issue 3

Full text available: pdf(320.62 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We present SLED, a specification language for Encoding and Decoding, which describes, abstract, binary, and assembly-language representations of machine instructions. Guided by a SLED specification, the New Jersey Machine-Code Toolkit generates bit-manipulating code for use in applications that process machine code. Programmers can write such applications at an assembly language level of abstraction, and the toolkit enables the applications to recognize and emit the binary representations u ...

Keywords: compiler generation, decoding, encoding, machine code, machine description, object code, relocation

3 Session 10B: Power saving techniques for embedded processors: I-CoPES: fast instruction code placement for embedded systems to improve performance and energy efficiency

Sri Parameswaran, Jörg Henkel

November 2001 **Proceedings of the 2001 IEEE/ACM international conference on Computer-aided design**

Full text available: pdf(170.07 KB)

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The ratio of cache hits to cache misses in a computer system is, to a large extent, responsible for its characteristics such as energy consumption and performance. In recent years energy efficiency has become one of the dominating design constraints, due to the rapid growth in market share for mobile computing/communication/internet devices. In this paper we present